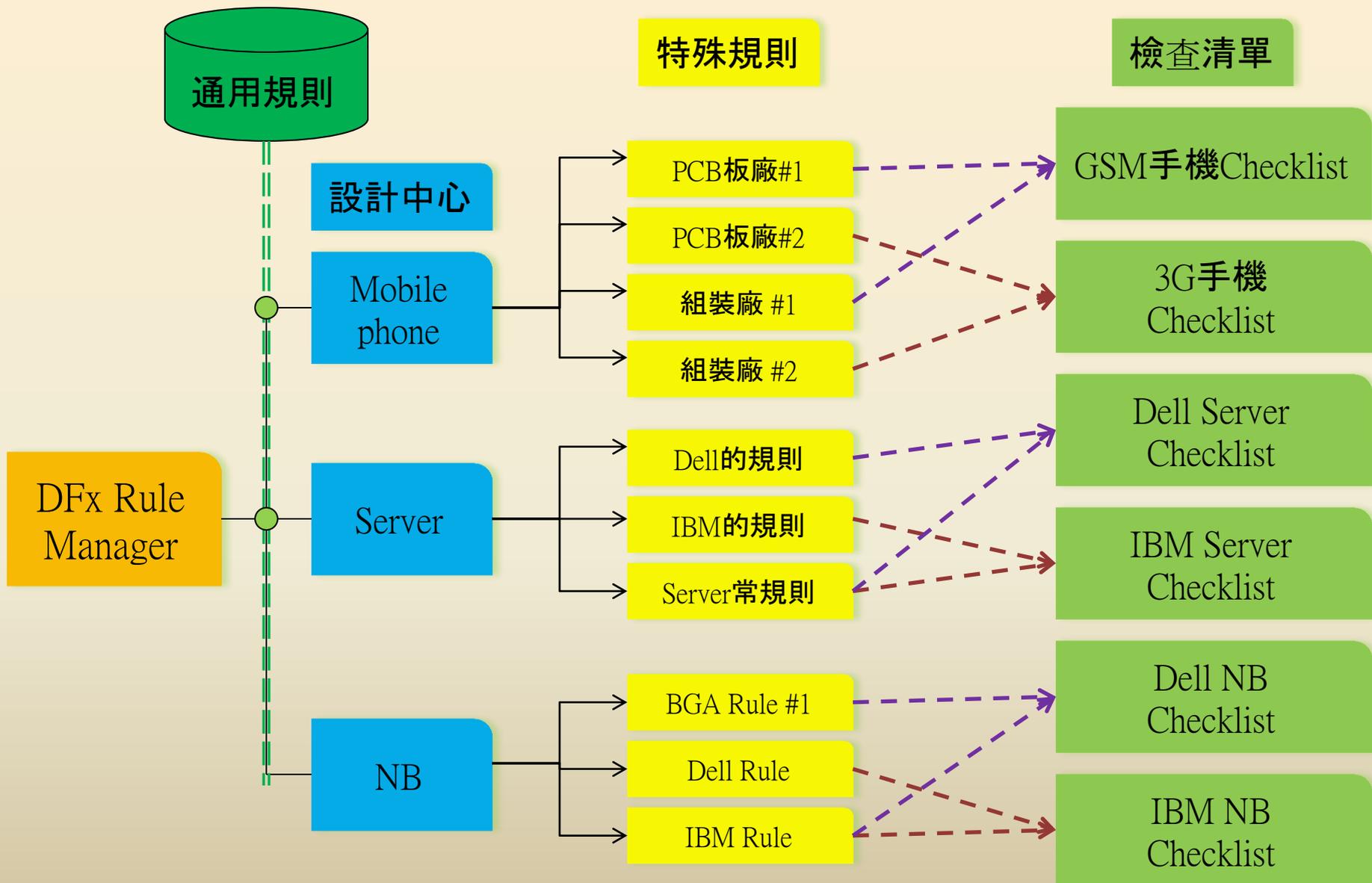


Valor NPI Auto Check

<http://www.peacegiant.com.tw>

一般公司的DFM檢查規範



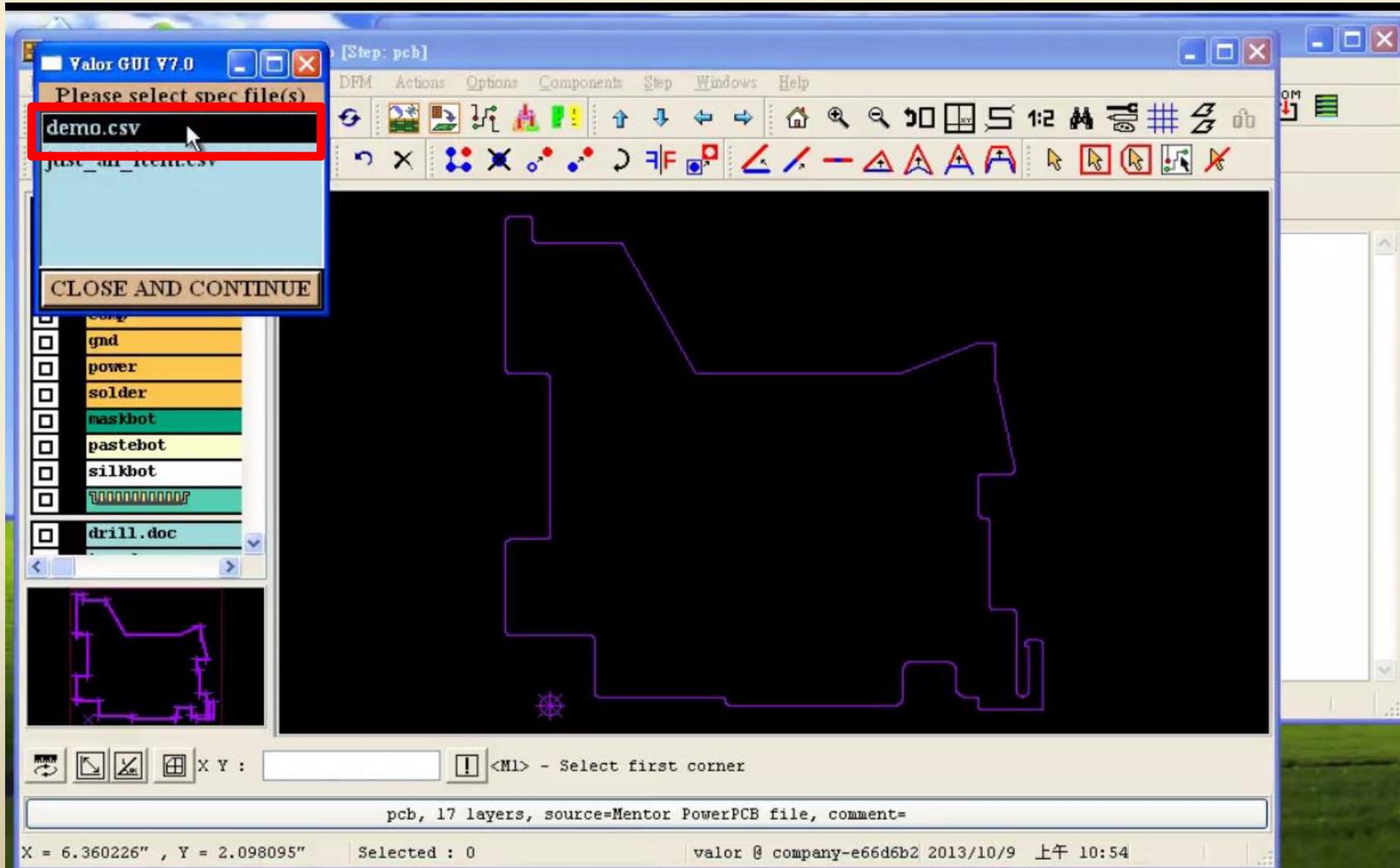
將DFM檢查規範存成.csv格式

剪貼簿	字型	對齊方式	數值
All	f _x		
A	B	C	D
1	Placement		
2	1	有極性電容只能有兩種方向	
3	2	有V-CUT時,板邊2mm內不可有任何零件; 無V-CUT時, 板邊4mm內不可有SMT零件	
4	3	BGA 文字框2mm內不可有SMT零件	
5	4	Hsink周圍3mm內不可有DIP零件	
6	5	檢查零件之間是否無擺放過近	
7	6	SLOT 的固定孔左右邊各100 mils內,不能放高零件	
8	★光學點、定位孔		
9	7	PCB正背板需有3個以上光學點	
10	8	折斷邊上需有4個定位孔	

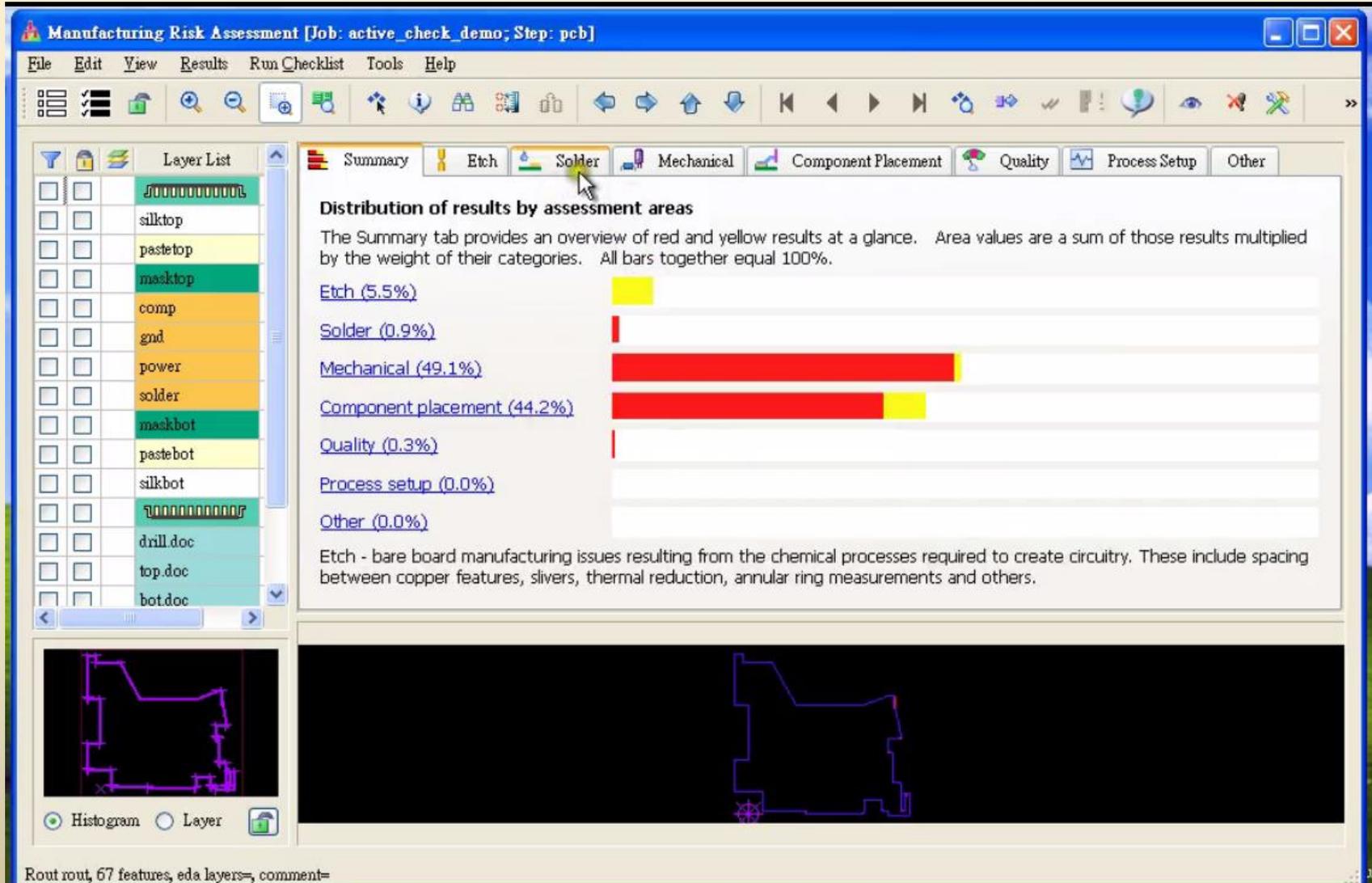
第 1 頁

Auto Check操作方式 I

在Valor NPI中呼叫.csv並自動執行檢查



自動執行檢查並產生結果



Manufacturing Risk Assessment [Job: active_check_demo; Step: pcb]

File Edit View Results Run Checklist Tools Help

Layer List

- silktop
- pastetop
- masktop
- comp
- gnd
- power
- solder
- maskbot
- pastebot
- silkbot
- drill.doc
- top.doc
- bot.doc

Summary Etch Solder Mechanical Component Placement Quality Process Setup Other

Distribution of results by assessment areas

The Summary tab provides an overview of red and yellow results at a glance. Area values are a sum of those results multiplied by the weight of their categories. All bars together equal 100%.

Assessment Area	Percentage
Etch	5.5%
Solder	0.9%
Mechanical	49.1%
Component placement	44.2%
Quality	0.3%
Process setup	0.0%
Other	0.0%

Etch - bare board manufacturing issues resulting from the chemical processes required to create circuitry. These include spacing between copper features, slivers, thermal reduction, annular ring measurements and others.

Route rout, 67 features, eda layers=, comment=



*Working together
&
Sharing success*